

EAST SEARCH

3/6/06

L#	Hits	Search String	Databases
S1	42773	computer aided design or CAD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S2	106	S1 and ((multiple or plurality) near2 designers)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S3	347	S1 and ((design or development) near2 team)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S4	205	S1 and (design near2 intent\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S5	29	S1 and (((first or one) near2 designer) with ((second or another or other) near2 designer\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S6	626	S2 or S3 or S4 or S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S7	221	S6 and (design near2 model)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S8	41	S6 and ((design near2 model) with (object or version))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S9	109	S6 and (designer with change\$1).	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S10	297	S6 and (design with (feature\$1 or parameter\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S11	4	S6 and (behavioral with parameter\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S12	0	S6 and ("functional model representation")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S14	1	S6 and (body near2 partitions)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S15	1	S6 and (contributing near2 volume\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S16	48	S6 and (digital near2 model)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S17	162	S6 and (model with database)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S18	117	S6 and (design near2 feature\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S19	5	S6 and ((identify or identification) with (design near2 feature\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S20	153	S6 and (feature\$1 with (location or time))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S21	117	S6 and (feature\$1 with (association\$1 or associat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S22	8	S6 and (destination with (client\$1 or device\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S23	130	S6 and (network with server\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S24	11	S6 and (communicat\$3 with asynchronous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S25	448	S7 or S10 or S17 or S20 or S23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S26	297	S8 or S9 or S11 or S13 or S14 or S15 or S16 or S18 or S19 or S21 or S22 or S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S27	278	S25 and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S28	297	S26 or S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S13	14	S6 and ("functional model")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S30	0	S29 and (S25 or S26)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S29	14	5,815,154.pn. or "5,995,097".pn. or "6,219,055".pn. or "6,628,279".pn. or "6,360,357".pn. or "6,628,279".pn. or "6,360,357".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S31	44801	computer aided design or CAD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S32	112	S31 and ((multiple or plurality) near2 designers)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S33	369	S31 and ((design or development) near2 team)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S34	34	S31 and (((first or one) near2 designer) with ((second or another or other) near2 designer\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S35	235	S31 and ((collaborative or collaboration) with design)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S36	638	S32 or S33 or S34 or S35	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

S37	35	S36 and (design near2 intent\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S38	208	S36 and (design with feature)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S39	84	S36 and (parameter with feature)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S40	187	S36 and (parameter\$3 with design)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S41	114	S36 and ((compar\$4 or analyz\$3 or analysis) with parameter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S42	178	S37 or S39 or S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S43	272	S38 or S40	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S44	138	S42 and S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S45	178	S42 or S44	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

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Results of search set S28

Document Kind	Codes	Title	Issue Date	Current OR	Abstract
US	20060004619	A1 Collaboration session recording model	20060105	705/8	
US	20050235246	A1 Use of models in integrated circuit fabrication	20051020	716/21	
US	20050209831	A1 Graphical object generation and regeneration	20050922	703/1	
US	20050209722	A1 System and method for automating architecture changes to system-level design	20050922	700/105	
US	20050204283	A1 System, method and article of manufacture for alerting a manager of a critical outstanding task	20050915	715/530	
US	20050196964	A1 Dummy fill for integrated circuits	20050908	438/692	
US	20050177531	A1 Method, tool and system for increasing the efficiency of a design process	20050811	706/45	
US	20050132306	A1 Characterization and reduction of variation for integrated circuits	20050616	716/1	
US	20050131924	A1 System and method for multi-dimensional organization, management, and manipulation of data	20050616	707/100	
US	20050119774	A1 Alternate ply representation for composite design and manufacturing	20050602	700/98	
US	20050076316	A1 Design-manufacturing interface via a unified model	20050407	716/4	
US	20050055181	A1 System, method, and storage medium for determining a packaging design for a container	20050310	703/1	
US	20050051809	A1 Dummy fill for integrated circuits	20050310	257/232	
US	20050050509	A1 Method, system and program product providing a configuration specification language that sup	20050303	716/18	
US	20050044514	A1 Method and platform for integrated physical verifications and manufacturing enhancements	20050224	716/5	
US	20050038821	A1 Method and system for capturing, managing and disseminating manufacturing knowledge	20050217	707/104.1	
US	20050037522	A1 Dummy fill for integrated circuits	20050217	438/8	
US	20050004774	A1 Methods and systems for inspection of wafers and reticles using designer intent data	20050106	702/108	
US	20040243481	A1 System and method for rapidly customizing design, manufacture and/or selection of biomedical	20041202	705/26	
US	20040221250	A1 PHYSICAL DESIGN CHARACTERIZATION SYSTEM	20041104	716/4	
US	20040204957	A1 Method and apparatus for evolutionary design	20041014	705/1	
US	20040194046	A1 Trace based method for design navigation	20040930	716/11	
US	20040186738	A1 Method and apparatus for an idea adoption marketplace	20040923	705/1	
US	20040177002	A1 Customer-based product design module	20040909	705/14	
US	20040158965	A1 Method and apparatus for assembling exterior automotive vehicle body components onto an ai	20040819	29/407.1	

US 20040153824 A1	System and method for creating and updating a three-dimensional model and creating a relate	20040805 714/38
US 20040143808 A1	Method of resolving mismatched parameters in computer-aided integrated circuit design	20040722 716/11
US 20040143807 A1	Method of resolving mismatched graphical symbols in computer-aided integrated circuit design	20040722 716/11
US 20040143799 A1	Method of resolving missing graphical symbols in computer-aided integrated circuit design	20040722 716/3
US 20040128648 A1	Face correlation between computer aided design models	20040701 717/105
US 20040113945 A1	Graphical user interface and method for interfacing with a configuration system for highly config	20040617 715/765
US 20040107125 A1	Business alliance identification in a web architecture	20040603 705/7
US 20040098292 A1	System and method for enabling supplier manufacturing integration	20040520 705/8
US 20040093397 A1	Isolated working chamber associated with a secure inter-company collaboration environment	20040513 709/219
US 20040084867 A1	Individualization of therapy with anticoagulants	20040506 280/163
US 20040068342 A1	Method of designing and manufacturing rubber process tooling using an interface to a CAD/CA	20040408 700/182
US 20040064351 A1	Increased visibility during order management in a network-based supply chain environment	20040401 705/7
US 20040015793 A1	Methodology for the optimization of testing and diagnosis of analog and mixed signal ICs and e	20040122 716/4
US 20040015367 A1	Business asset management system using virtual areas	20040122 705/1
US 20040006566 A1	System and method for augmenting knowledge commerce	20040108 707/100
US 20030229875 A1	Use of models in integrated circuit fabrication	20031211 716/10
US 20030229479 A1	Dummy fill for integrated circuits	20031211 703/14
US 20030228714 A1	Dummy fill for integrated circuits	20031211 438/8
US 20030226757 A1	Dummy fill for integrated circuits	20031211 205/82
US 20030208342 A1	Method and system for providing construction information service	20031106 703/1
US 20030200513 A1	Physical design characterization system	20031023 716/4
US 20030176994 A1	Boundary representation per feature methods and systems	20030918 703/27
US 20030103089 A1	Systems and methods for collaborative shape design	20030605 715/848
US 20030088458 A1	Method and apparatus for dynamic, real-time market segmentation	20030508 705/10
US 20030067487 A1	Intelligent constraint definitions for assembly part mating	20030410 715/764
US 20030060913 A1	Method and system for designing and modeling a product in a knowledge based engineering e	20030327 700/103
US 20030009727 A1	Circuit designing apparatus, circuit designing method and timing distribution apparatus	20030109 716/1
US 20020194581 A1	Methods and systems for automatically translating geometric data	20021219 717/136
US 20020194274 A1	System, method and article of manufacture for a communication manager in an integrated sch	20021219 709/205
US 20020188622 A1	Method and system for capturing, managing, and disseminating manufacturing knowledge	20021212 707/104.1
US 20020170160 A1	Method and apparatus for assembling exterior automotive vehicle body components onto an ai	20021121 29/407.1
US 20020165891 A1	System, method and article of manufacture for a resource and contact manager in an integrat	20021107 718/100
US 20020165723 A1	System, method and article of manufacture for managing a construction process	20021107 705/1
US 20020161783 A1	System, method and article of manufacture for a reports manager in an integrated scheduling :	20021031 707/200
US 20020143594 A1	System, method and article of manufacture for alerting a manager of a critical outstanding task	20021003 705/8
US 20020138391 A1	System, method and article of manufacture for a bid manager in an integrated scheduling and	20020926 705/37
US 20020138317 A1	System for implementing an exchange	20020926 705/7
US 20020133390 A1	System, method and article of manufacture for a document manager in an integrated schedulir	20020919 705/9
US 20020130903 A1	System, method and article of manufacture for a graphical sender interface for use in an integr	20020919 715/752
US 20020124028 A1	System, method and article of manufacture for scheduling and document management integra	20020905 715/530
US 20020123812 A1	Virtual assembly design environment (VADE)	20020905 700/98
US 20020120480 A1	System, method and article of manufacture for a project task manager in an integrated sched.	20020829 705/7
US 20020091923 A1	System, method, and medium for retrieving, organizing, and utilizing networked data using dat	20020711 713/168

US 20020083398 A1	Circuit designing apparatus, circuit designing method and timing distribution apparatus	20020627 716/1
US 20020073114 A1	Business asset management system	20020613 715/500
US 20020067364 A1	Method for browsing various intelligent design data abstractions	20020606 345/531
US 20020059049 A1	System and method for rapidly customizing design, manufacture and/or selection of biomedical	20020516 703/11
US 20020035450 A1	Network-based system for the manufacture of parts with a virtual collaborative environment for	20020321 703/1
US 20020016774 A1	Network enabled application software system and method	20020207 705/51
US 20020007348 A1	System and method for performing engineering design	20020117 705/51
US 20020007294 A1	System and method for rapidly customizing a design and remotely manufacturing biomedical d	20020117 705/7
US 20010052908 A1	Method and apparatus for computer aided building specification generation	20011220 345/630
US 7010544 B2	Method and system for capturing, managing and disseminating manufacturing knowledge	20060307 707/102
US 7010472 B1	Knowledge driven composite design optimization process and system therefor	20060307 703/6
US 7003757 B2	Dissection of edges with projection points in a fabrication layout for correcting proximity effects	20060221 716/19
US 7003742 B2	Methodology for the optimization of testing and diagnosis of analog and mixed signal ICs and e	20060221 716/3
US 6985835 B1	Method and apparatus for edge correlation between design objects	20060110 703/1
US 6983430 B2	Method of resolving mismatched parameters in computer-aided integrated circuit design	20060103 716/2
US 6968518 B2	Method of resolving missing graphical symbols in computer-aided integrated circuit design	20051122 716/3
US 6959268 B1	Product catalog for use in a collaborative engineering environment and method for using same	20051025 703/6
US 6957186 B1	System method and article of manufacture for building, managing, and supporting various com	20051018 705/1
US 6952688 B1	Knowledge-engineering protocol-suite	20051004 706/45
US 6952660 B1	Collaboration session recording model	20051004 703/1
US 6950995 B2	Method of resolving mismatched graphical symbols in computer-aided integrated circuit design	20050927 716/1
US 6931612 B1	Design and optimization methods for integrated circuits	20050816 716/5
US 6928396 B2	Network-based system for the manufacture of parts with a virtual collaborative environment for	20050809 703/1
US 6917907 B2	Method of power steering hose assembly design and analysis	20050712 703/8
US 6907573 B2	Intelligent constraint definitions for assembly part mating	20050614 715/764
US 6901570 B2	Method of generating optimum skew corners for a compact device model	20050531 716/6
US 6868297 B1	Method and system for designing objects using functional modeling	20050315 700/98
US 6836877 B1	Automatic synthesis script generation for synopsys design compiler	20041228 716/18
US 6828963 B1	Pattern matching for data exchange between computer aided design systems	20041207 345/419
US 6823496 B2	Physical design characterization system	20041123 716/4
US 6823342 B2	Method and system for capturing, managing, and disseminating manufacturing knowledge	20041123 707/102
US 6820247 B1	Method and apparatus for constructing a global interconnect model of a device having ambigu	20041116 716/13
US 6799174 B2	Retrieving, organizing, and utilizing networked data using databases	20040928 707/3
US 6792590 B1	Dissection of edges with projection points in a fabrication layout for correcting proximity effects	20040914 716/19
US 6775808 B1	Method and apparatus for generating sign-off prototypes for the design and fabrication of integ	20040810 716/4
US 6775647 B1	Method and system for estimating manufacturing costs	20040810 703/7
US 6775585 B2	Method and designing and manufacturing rubber process tooling using an interface to a CAD/C	20040810 700/182
US 6772026 B2	System and method for rapidly customizing design, manufacture and/or selection of biomedical	20040803 700/98
US 6763337 B1	Weighted wedge defuzzification for conceptual system design evaluation	20040713 706/9
US 6760693 B1	Method of integrating computer visualization for the design of a vehicle	20040706 703/8
US 6748284 B1	Feature based assembly	20040608 700/97
US 6721713 B1	Business alliance identification in a web architecture framework	20040413 705/1
US 6691392 B2	Method and apparatus for assembling exterior automotive vehicle body components onto an ai	20040217 29/407.09

US 6689035 B1	Method and apparatus for designing and creating a package	20040210 493/320
US 6686914 B2	Methods and systems for automatically translating geometric data	20040203 345/420
US 6678871 B2	Circuit designing apparatus, circuit designing method and timing distribution apparatus	20040113 716/6
US 6671818 B1	Problem isolation through translating and filtering events into a standard object format in a netv	20031230 714/4
US 6651037 B1	Method of optimizing design of an HVAC air-handling assembly for a climate control system	20031118 703/8
US 6640329 B2	Real-time connection error checking method and process	20031028 716/5
US 6625801 B1	Dissection of printed edges from a fabrication layout for correcting proximity effects	20030923 716/19
US 6618834 B2	Circuit designing apparatus, circuit designing method and timing distribution apparatus	20030909 716/2
US 6615166 B1	Prioritizing components of a network framework required for implementation of technology	20030902 703/27
US 6606744 B1	Providing collaborative installation management in a network-based supply chain environment	20030812 717/174
US 6578174 B2	Method and system for chip design using remotely located resources	20030610 716/1
US 6567819 B1	Run time objects	20030520
US 6546533 B1	Method and apparatus for specifying global abutment points in a semiconductor device having	20030408 716/10
US 6539521 B1	Dissection of corners in a fabrication layout for correcting proximity effects	20030325 716/4
US 6536037 B1	Identification of redundancies and omissions among components of a web based architecture	20030318 717/151
US 6536028 B1	Standard block architecture for integrated circuit design	20030318 716/17
US 6519571 B1	Dynamic customer profile management	20030211 705/14
US 6473794 B1	System for establishing plan to test components of web based framework by displaying pictori	20021029 709/223
US 6467074 B1	Integrated circuit architecture with standard blocks	20021015 716/17
US 6453457 B1	Selection of evaluation point locations based on proximity effects model amplitudes for correcti	20020917 716/19
US 6446243 B1	Method for functional verification of VLSI circuit designs utilizing reusable functional blocks or in	20020903 716/7
US 6421818 B1	Efficient top-down characterization method	20020716 716/18
US 6378123 B1	Method of handling macro components in circuit design synthesis	20020423 716/18
US 6366874 B1	System and method for browsing graphically an electronic design based on a hardware descrip	20020402 703/14
US 6341261 B1	Knowledge driven composite design optimization process and system therefor	20020122 703/6
US 6295636 B1	RTL analysis for improved logic synthesis	20010925 716/18
US 6295514 B1	Method, system, and computer program product for representing similarity/dissimilarity betwee	20010925 703/12
US 6295513 B1	Network-based system for the manufacture of parts with a virtual collaborative environment for	20010925 703/1
US 6292931 B1	RTL analysis tool	20010918 716/18
US 6292894 B1	System, method, and medium for retrieving, organizing, and utilizing networked data	20010918 713/168
US 6289498 B1	VDHL/Verilog expertise and gate synthesis automation system	20010911 716/18
US 6289491 B1	Netlist analysis tool by degree of conformity	20010911 716/5
US 6263483 B1	Method of accessing the generic netlist created by synopsys design compiler	20010717 716/18
US 6236409 B1	Method and apparatus for computer aided building specification generation	20010522 345/629
US 6216255 B1	Computer-aided logic circuit designing apparatus computer-aided logic circuit designing system	20010410 716/6
US 6205572 B1	Buffering tree analysis in mapped design	20010320 716/5
US 6173435 B1	Internal clock handling in synthesis script	20010109 716/18
US 6080204 A	Method and apparatus for contemporaneously compiling an electronic circuit design by contem	20000627 716/7
US 6055541 A	Automatic report generating system	20000425 707/103R
US 6038668 A	System, method, and medium for retrieving, organizing, and utilizing networked data	20000314 726/30
US RE36602 E	Concurrent engineering design tool and method	20000307 700/97
US 5999908 A	Customer-based product design module	19991207 705/1
US 5984511 A	Knowledge driven composite design optimization process and system therefor	19991116 703/6

US 5966310 A	Personal design system and personal equipment production system for actually producing equ	19991012 707/104.1
US 5903886 A	Hierarchical adaptive state machine for emulating and augmenting software	19990511 706/50
US 5822206 A	Concurrent engineering design tool and method	19981013 700/97
US 5784286 A	Design process recording method and a design process recorder	19980721 703/1
US 5598344 A	Method and system for creating, validating, and scaling structural description of electronic devi	19970128 716/18
US 5587914 A	Apparatus and method for computer-aided design of sheet metal fabricated parts	19961224 700/95
US 5572437 A	Method and system for creating and verifying structural logic model of electronic design from b	19961105 716/18
US 5572430 A	Method and apparatus for cooperated design	19961105 700/95
US 5557531 A	Method and system for creating and validating low level structural description of electronic desi	19960917 716/1
US 5555201 A	Method and system for creating and validating low level description of electronic design from hi	19960910 716/1
US 5553002 A	Method and system for creating and validating low level description of electronic design from hi	19960903 716/11
US 5552995 A	Concurrent engineering design tool and method	19960903 700/97
US 5544066 A	Method and system for creating and validating low level description of electronic design from hi	19960806 716/18
US 5541849 A	Method and system for creating and validating low level description of electronic design from hi	19960730 716/18
US 5307282 A	Method of computer-aided prediction of collisions between objects including fabrication tools ar	19940426 700/103
US 5247451 A	Method for establishing data defining a tool path for a numerically controlled machine without e	19930921 700/178
US 5106290 A	Assembly data model tool system	19920421 425/470
US 5033014 A	Integrated manufacturing system	19910716 703/1
US 5023800 A	Assembly data model system	19910611 700/182
US 4956764 A	Assembly data model tool system	19900911 700/59
US 4945488 A	Integrated aircraft manufacturing system	19900731 700/182
US 4937768 A	Integrated assembly system	19900626 703/1
US 4813013 A	Schematic diagram generating system using library of general purpose interactively selectable	19890314 715/763
US 4656603 A	Schematic diagram generating system using library of general purpose interactively selectable	19870407 715/835

Interference checked

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L1	11074	computer aided design or CAD	US-PGPUB
L2	38	1 and ((multiple or plurality) near2 designers)	US-PGPUB
L3	188	1 and ((design or development) near2 team)	US-PGPUB
L4	25	1 and (((first or one) near2 designer) with ((second or another or other) near2 designer\$1))	US-PGPUB
L5	121	1 and ((collaborative or collaboration) with design)	US-PGPUB
L6	304	2 or 3 or 4 or 5	US-PGPUB
L7	17	6 and (design near2 intent\$3)	US-PGPUB
L8	88	6 and (design with feature)	US-PGPUB
L9	29	6 and (parameter with feature)	US-PGPUB
L10	80	6 and (parameter\$3 with design)	US-PGPUB
L11	122	7 or 8 or 9 or 10	US-PGPUB
L12	38	11 and (parameter.CLM.)	US-PGPUB
L13	33	11 and (feature.CLM.)	US-PGPUB
L14	51	12 or 13	US-PGPUB

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US	20060004619 A1	Collaboration session recording model	20060105	705/8	
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US	20050209722 A1	System and method for automating architecture changes to system-level design	20050922	700/105	
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US	20050177531 A1	Method, tool and system for increasing the efficiency of a design process	20050811	706/45	
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US 20050051809 A1	Dummy fill for integrated circuits	20050310 257/232
US 20050050509 A1	Method, system and program product providing a configuration specification language that sup	20050303 716/18
US 20050044514 A1	Method and platform for integrated physical verifications and manufacturing enhancements	20050224 716/5
US 20050038821 A1	Method and system for capturing, managing and disseminating manufacturing knowledge	20050217 707/104.1
US 20050037522 A1	Dummy fill for integrated circuits	20050217 438/8
US 20050004774 A1	Methods and systems for inspection of wafers and reticles using designer intent data	20050106 702/108
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US 4656603 A	Schematic diagram generating system using library of general purpose interactively selectable	19870407 715/835	

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US 20060041840 A1	File translation methods, systems, and apparatuses for extended commerce	20060223 715/513	
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US 20060041503 A1	Collaborative negotiation methods, systems, and apparatuses for extended commerce	20060223 705/37	
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US 20030229479 A1	Dummy fill for integrated circuits	20031211 703/14	
US 20030228714 A1	Dummy fill for integrated circuits	20031211 438/8	
US 20030226757 A1	Dummy fill for integrated circuits	20031211 205/82	
US 20030212610 A1	System and method for specification and exchange management	20031113 705/26	

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US 20030090530 A1	Systems and methods for collaborative shape design	20030515 715/848
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